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Bilingual translation system capable of memorizing learned words

K Miyao, H Suzuki, H Asano, S Tokunaga ... - US Patent ..., 1989 - Google Patents

... OF MEMORIZING LEARNED WORDS [75] Inventors: Kouji Miyao, Kashihara; Hitoshi Suzuki, Nara; Hazime **Asano**, Kitakatsuragi; Shinji ... j WOrds as the learned word at the language and thereafter **converting** the inner construe- time of performing the **translation**, a display ...[Cached](#) · [15 - Related articles](#) · [All 2 versions](#) · [Import into BibTeX](#)NATURAL LANGUAGE PROCESSING DEVICE AND METHOD

Y ASANO, A HIROE, M SHIMAKAWA ... - WO Patent WO/2000/ ... , 2000 - wipo.int

... Inventors: **ASANO**, Yasuharu; (JP) ... **converting** the representation of the natural language, a display section for displaying a sentence the representation of which is converted by the representation **converting** section so as to make confirmation, a machine-**translation** section for ...[Cached](#) · [All 2 versions](#) · [Import into BibTeX](#)Speech recognizing method and apparatus, and speech translating system

Y Asano, H Ogawa, Y Kato, T Kaganishi ... - US Patent ..., 1998 - Google Patents

... AND SPEECH TRANSLATING SYSTEM [75] Inventors: Yasuharu **Asano**, Kanagawa; Hiroaki Ogawa ... 42 SPEECH SPEECH INPUT UNIT A/D **CONVERTING** UNIT ANALYZING ... DATABASE THESAURUS MEMORY UNIT TRANSLATING UNIT /ORIGINAL/ **TRANSLATION** WORD ...[Cached](#) · [24 - Related articles](#) · [All 2 versions](#) · [Import into BibTeX](#)[HTML] Int6/eIF3e promotes general **translation** and Atf1 abundance to modulate Sty1 MAPK-dependent stress response in fission yeast[\[HTML\]](#) [\[tex\]](#) [\[bibtex\]](#)

... N Jones, RC Wek, J Bähler, K Asano - Journal of Biological ... , 2005 - ASBMB

... study provides an important insight into the mechanism of gene regulation changes caused by the compromised activity of Int6/eIF3e-directed **translation** ... KAY456 was constructed by **converting** the leu1-32 and ura4-D18 loci of JY878 into leu1 + and ura4 +, respectively, as ...[Cached](#) · [5 - Related articles](#) · [All 10 versions](#) · [Import into BibTeX](#)Test program generation system and test program generation method for semiconductor test apparatus

Y Asano, Y Kotsuhiro, K Komuro ... - US Patent 6,601,393, 2003 - Google Patents

... GENERATION METHOD FOR SEMICONDUCTOR TEST APPARATUS (75) Inventors: Yoshihiro **Asano**, Yokohama (JP) ... At the **translator** 61, there are provided compile means 611 for compiling ... semiconductor test apparatus, comprising: a first step of **converting** data necessary ...

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Deletion mutagenesis of stem cell factor defines the C-terminal sequences essential for its biological activity

... K Katayama, N Fuji, K Ozawa, S Asano · Biochemical and ... 1992 · Elsevier

... b , Kanako Katayama b , Nobuyoshi Fuji b , Keiya Ozawa a and Shigetaka **Asano** b , a ... For the sequential deletion of 3' endoSCF cDNA, at first, universal **translation** terminator (Pharmacia ...

cDNA was inserted between BamHI and HindIII site of pBUT after **converting** SacI site of ...

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Fluctuation-free input buffer

N Masuda, M Asano, T Hayashi, H Tanaka ... · US Patent ... 1987 · Google Patents

... Hoffman, "Positive Voltage **Translation** Circuit", IBM TDB; vol. ... V as a low level and about -0.9 V as a high level, VERSION INPUT CIRCUIT" filed by Michio **Asano** that is ... Vff = W2 (Vcon, -Vssi - Vl = vin - VTl) - 55 60 65 That is, Vi becomes a voltage obtained by **converting** the level ...

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Cloning, nucleotide sequencing, and expression of an opine dehydrogenase gene from *Arthrobacter* sp. strain 1C

... Y Asano · Applied and Environmental Microbiology, 1995 · [.ncbi.nlm.nih.gov](#)

... keto acids [2]. Such a secondary amine dicarboxylic acid structure is found in angiotensin-**converting** enzyme inhibitors ... Electronic mail address: **asano**@pu-toyama.ac.jp ... sequence of the 2.1-kb PstI-XbaI fragment revealed an open reading frame, and **translation** of nucleotides 1 ...

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Level conversion input circuit

M Asano ... · US Patent 4,563,831, 1986 · Google Patents

... 7, 1986 [54] LEVEL CONVERSION INPUT CIRCUIT [75] Inventors: Michio **Asano**, Kokubunji, Akira ... 6/1984 Wrathall 307/475 OTHER PUBLICATIONS Hoffman, "Positive Voltage **Translation** Circuit", IBM ... & Wards [57] ABSTRACT An input circuit is provided for **converting** an ECL ...

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Wide-range TOC instrument using plasma oxidation

... Y Yamamoto, J Stillian, T **Asano** · US Patent App. 09/ ... , 2001 · Google Patents

... DE); Yasuo Yamamori, Longmont, CO (US); John Stillian, Longmont, CO (US); Tsutomu **Asano**, Oita (JP) ... it would be desirable to employ plasma oxidation as the technique for **converting** TOC in ... the plasma is not in equilibrium with the vibration, rotation or **translation** energy of ...

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